

# Anujha Ramasubramanian

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## EDUCATION

### Douglas College

New Westminster, Canada

Post-Degree Diploma in Information and Communication Technology

February, 2024

- **Relevant courses:** Advanced Integrated Software Development (Java), Data Structures and Algorithms, Introduction to programming(C#), Operating System, Virtualization and Computer Networks, Scripting for Cybersecurity, Fundamentals of Machine Learning
- GPA: 3.33

### CDAC

PG Diploma in Embedded System Design

Pune, India

February 2017

### MPSTME, NMIMS

Bachelor of Technology (Electronics and Telecommunication)

Mumbai, India

May 2016

- Cumulative GPA: 3.18 GPA: 3.45

## SKILLS

**Languages:** C, C++, Java, Python, Data Structures and Algorithm, Bash scripting(Intermediate), C#(Basic)

**Operating Systems:** Windows, Linux

**Version Control System:** GIT

**Protocols Know:** TCP/IP Network Protocol (Beginner)

**Micro-processors:** ARM Cortex M3, ARM7TDMI

**Micro-controllers:** STM32F103C8T6, AVR, PIC

**IDE:** Eclipse

**Editors:** Visual Studio Code, VIM

## WORK EXPERIENCE

### Dhrutel Technologies

Mumbai, India

Software Engineer

January 2020-October 2020

- Collaborated with senior on creating a platform for issuing real estate based crypto tokens using Quorum blockchain by setting up private blockchain network using **Docker** containers.
- Involved in research for AI applications for commercial underwriting.
- Involved in research for the Metatrader5 trading system.

### Giesecke + Devrient MS India Pvt Ltd

Pune, India

Giesecke & Devrient (G&D) is a technology leader in smart card production, and a solutions provider in a range of fields, including telecommunications, electronic payments, identification, health care, transportation and Internet security.

Intern

April 2017-October 2017

- Collaborated with senior on improving the various tests used by developer team written in C++.

Software Engineer

October 2017-October 2019

- Collaborated with developer team on development of operating system for embedded systems using C/C++ language for projects like Sm@rtSIM36, Sm@rtSIM37.
- Contributed to RAM and NVM optimization of OTA HTTP feature for Sm@rtSIM36.
- Improved the unit tests for testing OTA-HTTP feature post RAM and NVM optimization in Sm@rtSIM36 project.
- Engaged in resolving **JIRA** tickets for various projects raised by the testing team.
- Also wrote scripts for automation of testing different targets using **Python**. Used **Java** for applet development for projects.